



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Patent Application of

Soichi KOBAYASHI et al.

Application No.: 10/632,928

**Filed: August 4, 2003**

**For: SEMICONDUCTOR INTEGRATED  
CIRCUIT CAPABLE OF TESTING  
WITH SMALL SCALE CIRCUIT  
CONFIGURATION**

Group Art Unit: 2818

**Examiner:** Unassigned

Confirmation No.: 4942

**REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT**

**Commissioner for Patents  
Office of Initial Patent Examination  
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**Sir:**

Enclosed is a copy of the Official Filing Receipt marked in red to show error that is needed. The correction is as follows.


Under Assignment for Published Patent Application, delete [Renasas] and insert --Renasas--.

Issuance of a corrected Official Filing Receipt is respectfully requested.

Respectfully submitted,

~~BURNS, DOANE, SWECKER & MATHIS, L.L.P.~~

Date: November 25, 2003

By:   
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/632,928	08/04/2003	2818	750	009683-476	9	9	1

CONFIRMATION NO. 4942

21839  
BURNS DOANE SWECKER & MATHIS L L P  
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## FILING RECEIPT



\*OC000000011150074\*

Date Mailed: 10/31/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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## Assignment For Published Patent Application

~~Renesas~~ Technology Corp.;  
--Renesas--

Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2003-073951(P) 03/18/2003

If Required, Foreign Filing License Granted: 10/30/2003

Projected Publication Date: 09/23/2004

N n-Publication Request: No

Early Publication Request: No

## Title

Semiconductor integrated circuit capable of testing with small scale circuit configuration

Fukami Patent  
PNM/EME

BURNS, DOANE, SWECKER & MATHIS, L.L.P. RECEIVED
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